## B.Tech I Year (R13) Supplementary Examinations December/January 2015/2016

## BASIC ELECTRICAL \& ELECTRONICS ENGINEERING

(Common to CSE and IT)
Time: 3 hours

## Answer all the questions <br> *****

## PART - A

UNIT - I
1 (a) State and explain Kirchhoff's laws with suitable examples.
(b) Find the total resistance between A \& B terminals for the given networks.


2 (a) Define the following terms:
(i) Average value. (ii) RMS value. (iii) Form factor. (iv) Peak factor.
(b) A sinusoidal alternating current of 6 amps is flowing through a resistance of $40 \Omega$. Calculate the average voltage and the peak voltage of the supply.

> UNIT - II

3 (a) State superposition theorem. Explain it with an example.
(b) Explain the reciprocity theorem for DC excitation.

OR
4 (a) Explain about transmission and hybrid parameters and their relations.
(b) Find the hybrid parameters for the given two-port network.


5 (a) Explain the constructional details of DC Generators.
(b) Draw the characteristics of the DC Motors.

OR
6 (a) Derive the emf equation of DC Generator.
(b) Mention the advantages and applications of three phase induction motors.

## PART - B

## UNIT - I

With a neat diagram, explain the working of a PN junction diode in forward bias and reverse bias.

## OR

8 Draw the circuit diagram and explain the working of Half-wave rectifier and derive the expression for a ripple factor, efficiency, peak inverse voltage, transformer utilization factor, form factor and peak factor.

## UNIT - II

9 (a) Explain the operation of NPN and PNP transistor.
(b) Find the relationship between $\alpha$ and $\beta$.

## OR

10 (a) Explain the construction of N channel JFET.
(b) Compare JFET and BJT.

## UNIT - III

11 (a) Derive the expression for the efficiency of oscillation and the minimum gain required for sustained oscillations of the RC phase shift oscillator.
(b) In an RC phase shift oscillator if $R_{1}=R_{2}=R_{3}=200 \mathrm{k} \Omega$ and $C_{1}=C_{2}=C_{3}=100 \mathrm{PF}$. Then find the frequency of oscillator.

OR Draw the adder-subtractor circuit using Op-amp and explain its operation.

