[10]

[10]

[10]

[10]

Code No: 126EQ

8.

9.

10.

11.

with an example.

Discuss about artifact diagrams.

## JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, October/November - 2016 OBJECT ORIENTED ANALYSIS AND DESIGN

(Common to CSE, IT)

Time: 3 hours Max. Marks: 75 **Note:** This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART - A **(25 Marks)** 1.a) Discuss the importance of modeling. [2] Explain the structural things in UML. b) [3] Distinguish between interface and class. [2] c) Discuss various uses of object diagram. d) [3] Explain with an example how to depict an asynchronous message. [2] e) f) Distinguish between action state and activity state. [3] Distinguish signals and active classes. [2] g) How to model life time of an object? h) [3] What is meant by framework? [2] i) Discuss the significance of patterns. **i**) [3] PART - B **(50 Marks)** 2. State and explain the common mechanisms in the UML. [10] 3. Discuss about modeling a systems architecture. [10] 4. Discuss how classes can be used for modeling the non software things and modeling primitive types. [10] OR 5. Explain with an example about modeling structured relationships. [10] Explain with an example how to model flow of control by time and flow of control by 6. organization. [10] OR 7. How to model requirements of a system. Explain with an example. [10]

Write the class diagram for the university library application.

Explain about communication and synchronization with a suitable example.

What is meant by state machine? Discuss about sequential substates and history states