[5+5]

[5+5]

Code No: 126VR

6.a)

7.a

b)

b)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, December - 2019 SOFTWARE TESTING METHODOLOGIES

(Common to CSE, IT)

Time: 3 hours Max. Marks: 75 **Note:** This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART - A **(25 Marks)** 1.a) What is software testing and write its purpose. [2] Write the applications of path testing. b) [3] What is Data flow anomaly? c) [2] Compare data flow Vs Transaction flow. [3] d) What is loop free software? e) [2] Write about linear vector space. f) [3] Define cross and parallel term in path testing. [2] g) Write the limitations of path testing. h) [3] What is state transition? i) [2] Describe encoding bugs and give examples. i) [3] PART - B **(50 Marks)** 2.a) What is bug? Explain various kinds of bugs. Explain in detail about path sensitization. b) [5+5]List the elements of flow graph and explain each element with suitable diagram. 3.a) What is path testing? Give a note on path selection and predicates. b) [5+5]4.a) What is transaction flow testing? Explain with example. What is transaction instrumentation in transaction flow? Explain with example. b) [5+5]OR What is program slicing? Explain Dynamic program slicing. 5.a) Explain different data object states in data flow graphs. b) [5+5]

Write about Nice and ugly domains and give examples to each domain. Explain various bugs encountered at systematic and domain boundaries.

Explain predicates of domain testing with examples.

Compare domain testing and interface testing.

8.a)	Write the procedure to count minimum number of paths in a graph.	
b)	Describe push/pop and get/return models in path testing.	[5+5]
	OR	
9.a)	Compare structured and unstructured flow graphs and illustrate with an example.	
b)	What is KV-Chart? Draw KV-chart for 4 variables.	[5+5]
10.a)	Explain state testing in detail.	
b)	Write the guidelines to design state machines.	[5+5]
	OR	
11.a)	Elaborate node reduction algorithm with an example.	
b)	Explain good state graph with suitable example.	[5+5]

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