Code No: R1631051

III B. Tech I Semester Supplementary Examinations, Dec/Jan – 2022-23 **COMPILER DESIGN**

(Computer Science & Engineering)

Time: 3 hours Max. Marks: 70 Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**) 2. Answer **ALL** the question in **Part-A** 3. Answer any **FOUR** Questions from **Part-B** PART -A (14 Marks) [2M]Define regular expression. Give example. b) Write the rule to find the first and follow function. [2M][2M] List the properties of LR parser. [3M] List various intermediate code representations. [3M] Write the fields and uses of symbol table. [2M] Compare local optimization with global optimization. PART -B **(56 Marks)** What do you mean by front end in the compiler design? Show the output [9M] produced by it in different stages for a:=b*c/36; where a, b and c are real numbers. b) Write a regular expression for identifiers and reserved words. Design the [5M] transition diagrams for them. Eliminate left recursion in the following grammar [5M] $A \rightarrow ABd \mid Aa \mid a$, $B \rightarrow Be \mid b$ b) Construct the collection of LR(0) item sets and draw the goto graph for the [9M] grammar S -> S S | a | ϵ . Indicate the conflicts (if any) in the various states of the SLR parser. a) Write and explain the Syntax Directed definition for the grammar E→ [7M] E1+T/E1-T/T, $T \rightarrow (E)/id/num$. [7M] Explain the process of handling "Dangling-ELSE" ambiguity. [7M] Define Type Checker. Write down the specification of a simple Type Checker b) Write the quadruple, triple, indirect triple for the expression [7M] X=(a*b) + (c+d)-(a+b+c+d)What is an activation record? Explain how it is related with run time storage [7M] organization? b) Write the algorithm to generate basic blocks and flow graph for quick sort [7M] algorithm. 7. a) What is machine independent optimization? What are the different techniques [7M] used for it. [7M]

b) What is Peephole optimization? Explain its characteristics.