Code No: R31051 $\,\,$ R10

Set No. 1

III B.Tech I Semester Supplementary Examinations, October/November - 2017 COMPILER DESIGN

(Computer Science and Engineering)

Time: 3 hours Max. Marks: 75

Answer any FIVE Questions All Questions carry equal marks

All Questions carry equal marks *****			
1		Describe the output for the various phases of compiler with respect to the following statements count = $count *2 + count *2$.	[15M]
2	a)	Why is buffering used in lexical analysis? What are the commonly used buffering methods?	[7M]
	b)	Construct the minimized DFA for the regular expression (0+1)*(0+1) 10	[8M]
3		What are the key problems with top down parser? Write recursive descent parser for the grammar: S→cAd A→abla	[15M]
4		Construct a SLR parser for the following grammar: Stmts \rightarrow Stmt Stmts; Stmt Stmt \rightarrow Var = E Var \rightarrow id [E] Var \rightarrow id \rightarrow	[15M]
5	a)	Describe various steps in the construction of LALR parser. Explain reduce-reduce	[8M]
	b)	conflict with an example. How to handle the errors in LR parsing? Explain with an example.	[7M]
6		Write syntax directed translation to translate the following statements i) if ii) Ifelse iii) while and iv) for statements into three address code.	[15M]
7	a) b)	What are the different loop optimization methods? Explain them with examples. What is an activation record? Explain how it is relevant to the intermediate code generation phase with respect to procedure declarations.	[8M] [7M]
8	a) b)	What is the use of DAG in code optimization? Explain with an example. What is the optimization technique applied on procedure calls? Explain with an example. *****	[7M] [8M]