

Code No: **R31055**

**R10**

**Set No. 1**

**III B.Tech I Semester Supplementary Examinations, May - 2016**

**MICRO PROCESSORS AND MULTICORE SYSTEMS**

**(Computer Science and Engineering)**

**Time: 3 hours**

**Max. Marks: 75**

**Answer any FIVE Questions  
All Questions carry equal marks**

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- 1 a) Explain the register set of 8086 processor. [8]  
b) What is addressing mode? Explain different type of addressing modes in 8086 with examples. [7]
- 2 a) Explain the implementation of jumps and conditional jumps in assembly language with example programs. [10]  
b) Write assembly language instructions to implement delay of 10msec. [5]
- 3 a) List the string Instructions and Explain with suitable examples. [8]  
b) Write an ALP in 8086 to exchange two blocks of 8 bit numbers using string instructions. [7]
- 4 a) List and explain Bit manipulation instructions of 8086 with examples. [7]  
b) What are assembler directives? Explain the significance of the following assembler directives with suitable examples i) LENGTH ii) TYPE iii) DB iv) EQU. [8]
- 5 a) Explain briefly about interrupt structure and its importance. [8]  
b) Describe the response of 8086 processor, when interrupt coming on INTR. [7]
- 6 a) Write an ALP in 8086 to divide a 32-bit number by a 16-bit numbers. [8]  
b) Write an ALP to convert a 3- digit BCD number to binary number. [7]
- 7 a) Explain the control register structure of 80386 and explain what are debug and test register. [8]  
b) Explain briefly about branch prediction method. [7]
- 8 a) Draw the flag register of 80486 processor and explain the function of each flag in detail. [8]  
b) Explain the features of Dual core processor in details. [7]

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