

Code No: **R31055**

**R10**

**Set No. 1**

**III B.Tech I Semester Supplementary Examinations, May - 2018**  
**MICROPROCESSORS AND MULTICORE SYSTEMS**  
**(Computer Science and Engineering)**

**Time: 3 hours**

**Max. Marks: 75**

**Answer any FIVE Questions**  
**All Questions carry equal marks**

\*\*\*\*\*

- 1 a) What is a microcomputer? Draw and explain the basic block diagram of a microcomputer. [8M]  
b) Explain different assembly language development tools. [7M]
- 2 a) Explain the implementation of jumps and conditional jumps in assembly language with example programs. [8M]  
b) Write assembly language instructions to implement a delay of 10 msec. [7M]
- 3 a) List and explain the different string instructions of 8086. [8M]  
b) What do you mean by a macro? With a suitable example, explain the definition of a macro. [7M]
- 4 What are assembler directives? List and explain different assembler directives with suitable examples. [15M]
- 5 a) Define interrupt? Discuss in detail about the interrupt structure of 8086. [7M]  
b) Explain vector interrupt table of microprocessor. [8M]
- 6 a) Write an assembly language program to find out whether the given string is palindrome or not. [7M]  
b) Write an assembly language program to convert a BCD number to ASCII number. [8M]
- 7 a) List the salient features of the 80386 microprocessor. [7M]  
b) Explain the real mode and protected mode operations of 80386 microprocessor. [8M]
- 8 a) With suitable diagram explain the memory organization of Pentium processor. [8M]  
b) Compare the Dual core processor architecture with the Pentium architecture. [7M]

\*\*\*\*\*

[WWW.MANARESULTS.CO.IN](http://WWW.MANARESULTS.CO.IN)

