Code No: **R32052** **R10** Set No. 1

III B.Tech II Semester Supplementary Examinations, April - 2016
COMPUTER ARCHITECTURE
(Computer Science and Engineering)

Time: 3 hours   Max. Marks: 75

Answer any FIVE Questions
All Questions carry equal marks
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1	a)	Give a brief note on SIMD Supercomputers.	[7]
	b)	Discuss the Evolution of Computer Architecture.	[8]
2		Explain the need for having a hierarchical memory organization and explain the hierarchy in detail with a block diagram.	[15]
3	a)	Briefly explain the Design space of processors.	[8]
	b)	Write the Characteristics of typical RISC architecture processor.	[7]
4	a)	List and explain the types of pipelined processor.	[7]
	b)	Explain various phases involved in Instruction execution.	[8]
5	a)	Draw and explain the Crossbar Switch and Multiport Memory.	[7]
	b)	Explain the Multistage and Combining Networks with a neat diagram.	[8]
6	a)	Discuss the three approaches to Multi-cast Routing.	[7]
	b)	How to use virtual channel for breaking deadlock?	[8]
7	a)	Explain MIMD with Shared Memory with a neat diagram.	[7]
	b)	Describe the Symmetric Multiprocessors with an example.	[8]
8		Make a comparison between the Structural Parallelism versus Instruction Level Parallelism.	[15]

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