Total No of Questions: [8]		of Questions: [8] SEAT NO. :	
		[Total No. of Pag	es : 1 ]
		S. E. 2012 (E&TC/Electronics)204182	
		Electronic Devices and Circuits	
		(Semester - I)	
Tim	Time: 2 HoursMax. Marks		
Instru	ctions	to the candidates:	
<i>1)</i> 2)	Attem Neat	pt Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8. diagrams must he drawn wherever necessary	
$\begin{pmatrix} 2 \\ 3 \end{pmatrix}$	Figur	es to the right side indicate full marks.	
4)	Use o	f Calculator is allowed.	
5)	Assun	ne Suitable data if necessary	
Q1)	a)	Explain what is meant by Thermal Runaway in BJT circuits.	[6]
	b)	The transistor is connected in CE amplifier with bypassed $R_E$ has $R_1 = 50$ K $\Omega$ ,	[6]
		$R_2 = 2 K\Omega$ , $R_C = 1 K\Omega$ , $R_S = 1 K\Omega$ , $R_L = 10 K\Omega$ . Also h-parameters are $h_{ie} = 1.1$	
		$K\Omega$ , $h_{fe} = 50$ , $h_{oe} = 24 \ \mu A/V$ , $h_{re} = 2.5 \times 10^{\circ}$ . Determine the value for $A_v$ , $A_I$ , $R_i^{\circ}$ and $R_i^{\circ}$	
		OR	
Q2)	a)	Determine the operating point and draw DC and AC Load Line if $V_{CC} = 12 \text{ V}, \text{ R}_1$	[6]
		= 8 K $\Omega$ , R <sub>2</sub> = 4 K $\Omega$ , R <sub>E</sub> = 1 K $\Omega$ , R <sub>C</sub> = 1 K $\Omega$ , R <sub>L</sub> = 1.5 K $\Omega$ . Assume V <sub>BE</sub> = 0.7 V.	
	b)	Compare CE, CB and CC amplifier performance parameters.	[6]
Q3)	a)	Draw hybrid – $\pi$ CE Amplifier model at high frequency. Explain significance of each parameter.	[6]
	b)	An amplifier has gain of 60 and distortion is 10 % without feedback. Determine:	[6]
		(i) Gain, (ii) Distortion, when negative feedback is applied. Assume feedback	
		factor as 0.1	
(04)	a)	<b>UK</b> Explain the effect of Emitter bypass capacitor on low frequency response of BIT	[6]
21)	u)	amplifier.	[0]
	b)	Explain RC Phase Shift Oscillator using BJT and determine the output frequency	[6]
		for $R = 1 \text{ K}\Omega$ , $C = 0.01 \mu\text{F}$ .	
Q5)	a)	In a Class A amplifier $V_{CE (max)} = 25 \text{ V}, V_{CE (min)} = 5 \text{ V}$ . Find the overall efficiency	[6]
		for: (i) series fed load, (ii) transformer load.	
	b)	With the help of neat circuit diagram, explain the operation of Class AB power	[7]
		amplifier. Explain the significance of Class AB.	
		OR	
Q6)	a)	Draw and explain Transformer coupled Audio Power Amplifier. Derive the	[6]
	1 \	expression for its efficiency.	[7]]
	D)	A Class B push pull amplifier is supplied with $v_{CC} = 50$ v. The signal swings the collector voltage down to $V_{cc} = 10$ V. The total power dissipation in both	[/]
		transistors is 40 W. Find: i) $P_{in}(d_0)$ ii) $P_{a(d_0)}$ iii) % n	
Q7)	a)	Describe the internal capacitance and high frequency model of MOSFET.	[6]
	b)	E-MOSFET biased in CS configuration has following parameters: $R_1 = 10 M\Omega$ ,	[7]
		$R_2 = 6.8 \text{ M}\Omega$ , $R_D = 2.2 \text{ k}\Omega$ , $V_{DD} = 24 \text{ V}$ , $V_T = 3 \text{ V}$ , $I_{D(ON)} = 5 \text{ m}A$ , $V_{GS(ON)} = 6 \text{ V}$ .	
		Determine the values for $I_D$ and $V_{DS}$ .	
08)	a)	Enlist biasing of EMOSFET in common source configuration and explain any	[6]
		one of them in detail manaresults co in	L - J
	b)	Explain various non-ideal current voltage characteristics of EMOSFET.	[7]