Total No	o. of Questions : 10]	SEAT No.:
P3508	[5560]-158	[Total No. of Pages : 2
	TE (E & TC)	
	EMBEDDED PROCES	SOR
	(2012 Pattern) (Semeste	r - II)
Time: 2	½ Hours]	[Max. Marks : 70
Instructi	ions to the candidates:	
1)	Answer Q1 or Q2, Q3 or Q4, Q5 or Q6, Q7 or Q8	8 and Q9 or Q10.
2)	Neat diagrams must be drawn wherever necessar	y.
<i>3</i>)	Figure to the right indicate full marks.	
4)	Assume suitable data, if necessary.	
01) a)	Draw and explain ARM 7 data flow mode	1. [6]

OR

Q2) a) Draw and explain memory map of LPC 2148. [6]
b) Draw and explain format of GPSR register of ARM 7. [4]

Explain IOSET and IOCLR Registers of LPC 2148.

b)

Q3) a) Draw interfacing diagram and write an embedded C program to flash 8LEDs connected at P0.8 to P0.15 of LPC 2148.[6]

b) Explain SD card interfacing with LPC 2148. [4]

OR

Q4) a) Draw interfacing diagram and write an embedded 'C' program to display string 'SPPU' on 16*2 LCD.[6]

b) With the help of interfacing diagram explain GSM interfacing with LPC 2148. [4]

Q5) a) Compare ARM CORTEX A, CORTEX M, CORTEX R processors.[8]

b) Explain CMSIS standard of ARM. [8]

OR

P.T.O.

[4]

Q6) a)	Explain registers used in CORTEX M3 processor.	[8]
b)	Explain thread and handler modes of Cortex M3.	[8]
Q7) a)	State feature of LPC 1768.	[8]
b)	Interface two 7 segment display to LPC 1768 and write a 'C' prodisplay digits '54' on them.	gram to
	OR	
Q8) a)	Draw & explain block diagram of LPC 1768.	[8]
b)	Interface RGB LED to LPC 1768 & Write a 'C' program to display red blue and green colour with some delay. [8]	
Q9) a)	Explain USB communication.	[9]
b)	Explain PIN connects black of LPC 1768 & registers associate this block.	ed with [9]
	OR	
Q10)a)	Explain CAN protocol in details.	[9]
b)	Explain Ethernet based communication.	[9]