



C09-EC-603

**3759**

**BOARD DIPLOMA EXAMINATION, (C-09)**

MARCH / APRIL - 2019

**DECE - VI SEMESTER EXAMINATION**

**MICRO CONTROLLERS**

Time : 3 Hours]

[Total Marks : 80

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**PART - A**

**3×10=30**

**Instructions :**

- (1) Answer **ALL** questions.
- (2) Each question carries **THREE** marks.
- (3) Answer should be brief and straight to the point.

- 1 Compare Microprocessors and Micro controllers.
- 2 Define Stack and Stack pointer.
- 3 Explain the terms Operation code, Operand with examples.
- 4 Classify the instruction set of 8051.
- 5 List any six Logical group of instructions.
- 6 What is a Flowchart ? List four symbols in it.
- 7 Define Subroutine and its use.
- 8 Explain the need for Interfacing.
- 9 Draw the Pin assignments of RS-232C.
- 10 State the features of 8251.

3759 ]

1

[ Contd...

**PART - B****10×5=50**

- Instructions :**
- (1) Answer any **FIVE** questions.
  - (2) Each question carries **TEN** marks.
  - (3) Answer should be comprehensive and the criterion for valuation is the content but not the length of the answer.

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|-----------|--|--|
| <b>11</b> | Draw the pin diagram of 8051 Microcontrollers and explain the functions of each pin ?  | <b>5+5</b>                                   |
| <b>12</b> | (a) Explain the internal memory organization of 8051.<br>(b) What are the interrupts of 8051 and list them ?   | <b>6+4</b>                                   |
| <b>13</b> | (a) Explain the following instructions :<br>(i) MOVX @DPTR,A<br>(ii) MOV @RI,A<br>(iii) DAA<br>(iv) JMP @A+DPTR<br><br>(b) Explain various conditional jump instructions.  | <b>4</b><br><br><br><br><br><br><br><b>6</b> |
| <b>14</b> | Explain the addressing modes of 8051 with examples.  | <b>10</b>                                    |
| <b>15</b> | (a) Explain the concept of nesting and common ending of a subroutine.<br>(b) Write a program to perform 2's complement of an 8-bit number stored in the memory location at an address 1,400H and store the result in the location at an address 1401H. | <b>5</b><br><br><b>5</b>                     |
| <b>16</b> | State the sequence of operation involved in PUSH and POP instructions.   | <b>10</b>                                    |
| <b>17</b> | Draw the block diagram Of 8251 and explain.  | <b>10</b>                                    |
| <b>18</b> | Draw and explain block diagram of 8257 programmable DMA Controller.  | <b>10</b>                                    |